Winter Term				
Week#	Lessons	Homework		
Start Date	Left side number corresponds to weekly lessons	Red is pre-lesson preparation		
	(1 = first lesson of the week)	Black is due 8pm Tuesday		
1	1 Intro to the course			
09.09.24	2 September Test – Summer Work			
	3 Core Concepts 1			
_	4 Core Concepts 2			
2	1 Core Concepts 3	Exercise 0		
16.09.24	2 Core Concepts 4	September Test Corrections		
	3 Core Concepts 5 4 Core Concepts 6			
3	1 Core Concepts 7	Timing 1, 2, 3		
23.09.24	2 Timing Circuits 1, 2, 3	Timing 4		
Fri:	3 Timing Circuits 1, 2, 3 3 Timing Circuits Building: Capacitor charge/discharge	Exercises 2.1, 2.2, 2.3, 2.4		
Induction	4 Timing 4	GCSE Basics 1 2.1, 2.2		
Resit	7 1111111111111111111111111111111111111	Thevenin's Worksheet		
4	1 Timing Circuits 5	Timing 5		
30.09.24	2 Timing Circuits Building: 555 Monostable	Timing 6		
	3 October Test – Core Concepts	Exercises 2.5, 2.6		
	4 Timing Circuits 6	GCSE Basics 1 2.3, 2.4		
5	1 Timing Circuits 7	Timing 7		
07.10.24	2 Timing Circuits Building: 555 Astable	Exercises 2.7		
	3 October Test Feedback	GCSE Basics 2 3.1, 3.2		
	4 Timing Circuits Building: Schmitt Inverter Astable	October Test Corrections		
6	1 Logic 1	Logic 1, 2, 3		
14.10.24	2 Logic 2	Exercises 1.1, 1.2, 1.3		
ABT out	3 Logic Circuit Building	GCSE Basics 2 3.3, 3.4		
	4 Logic 3			
7	1 Logic 4	Logic 4, 6, 5		
21.10.24	2 Logic 6	Exercises 1.4, 1.6, 1.5		
ABT out	3 Logic Circuit Building	GCSE Basics 2 3.5 Basics 3 3.1		
	4 Logic 5			
8	1 Logic 7	Logic 7		
o 04.11.24	2 Flowcharts 1	Flowcharts 1		
Mon INSET	3 Logic 8	Logic 8		
ABT out	3 Logic o	Exercises 1.7, 1.8		
Abroat		GCSE Timing 1.1, 1.2		
9	1 Logic 9, 10, 11	Logic 9, 10, 11		
11.11.24	2 (Only D-block) Logic Circuit Building	Exercises 1.9, 1.10, 1.11		
Thurs PR	3 (Only D-block) Logic Circuit Building	GCSE Flowchart 4.1		
Fri INSET	4 (Only D-block) Logic Circuit Building	GCSE Timing 1.3, 1.4		
ABT out				
10	1 November Test – 50% Timing, 50% prior content	Logic 12		
18.11.24	2 Logic 12	Flowcharts 2		
ABT out	3 Flowcharts 2	Logic 13		
	4 Logic 13	Exercises 1.12, 1.13		
		GCSE Timing 1.5		
4.4	10.4	0.46		
11	1 Op-Amps 6: Comparators	Op-Amps 6		
25.11.24	2 November Test Feedback	Flowcharts 3		
ABT out	3 Flowcharts 3	Op-Amps 1		
	4 Op-Amps 1: Ideal V Real Op-Amps	Exercises 4.6, 4.1 GCSE Flowchart 4.2		
		GCSE Flowchart 4.2 GCSE Logic 6.1, 6.2		
		November test corrections		
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12	1 Op-Amps 3: Voltage Follower + Non-Inverting	Op-Amps 3
02.12.24	2 Op-Amps Circuit Building	Flowcharts 4
ABT out	3 Flowcharts 4	Op-Amps 2
	4 Op-Amps 2: Inverting	Exercises 4.3, 4.2
		GCSE Logic 6.3, 6.4
13	1 December Test – 50% Logic, 50% prior content	Op-Amps 4
09.12.24	1 Op-Amps 4: Summing	Flowcharts 5
ABT out	3 Flowcharts 5	Op-Amps 5
	4 Op-Amps 5: Distortion	Exercises 4.4, 4.5
	Top / unipo or biotortion	GCSE Flowchart 4.3
		GCSE Logic 6.5, 6.6
14	1 December Test Feedback	GCSL LOGIC 0.5, 0.0
16.12.24	2 (Only D-block) Op-Amp Circuit Building	
Term ends	3 (Only D-block) Op-Amp Circuit Building	
<mark>Weds</mark>	4 (Only D-block) Op-Amp Circuit Building	
ABT out		
	Christmas Break	
	Spring Term	
	Spring reim	
15	1 Op-Amp Circuit Building	Sequential Logic 1
06.01.25	2 Flowchart Coursework 1: Introduction and Plan	Exercise 3.1
	3 Sequential Logic 1	GCSE Op-Amps 1 5.1
	4 Sequential Logic Circuit Building: NAND Gate	Gest of / mps 1 3.1
16	1 January Test – 50% Op-Amps, 50% prior content	Flowchart C/W Plan
13.01.25	2 Flowcharts Coursework 2: Plan Feedback	
		Sequential Logic 2
Tues	3 Sequential Logic 2	Exercise 3.2
PR Day	4 (Only F-block) Sequential Logic Circuit Building	GCSE Op-Amps 1 5.3
17	1 January Test Feedback	Sequential Logic 3
20.01.25	2 Flowcharts Coursework 3: Development	Exercise 3.3
	3 Sequential Logic 3	GCSE Op-Amps 1 5.4
	4 Sequential Logic Circuit Building: Data Latch	January Test Corrections
18	1 Sequential Logic 4	Sequential Logic 4
27.01.25	2 Flowcharts Coursework 4: Development Feedback	Flowchart C/W Development
27.01.23	·	
	3 Sequential Logic 5	Sequential Logic 5
	4 Seq. Logic Circuit Building: Data Latch Counters	Exercises 3.4, 3.5
		GCSE Op-Amps 1 5.5
19	1 Sequential Logic 6 and 7	Sequential Logic 6 and 7
03.02.25	2 Flowcharts Coursework 5: Realisation and Evaluation	Sequential Logic 8
	3 Sequential Logic 8	Exercises 3.6, 3.7, 3.8
	4 Seq. Logic Circuit Building: Modulo-n	GCSE Op-Amps 2 4.4
20	1 Semiconductors 1 and 2	Semiconductors 1 and 2
10.02.25	2 Flowcharts C/W 6: Realisation and Evaluation Feedback	F. C/W Realisation & Evaluation
	3 Semiconductors 3	Semiconductors 3
	4 Semiconductor Circuit Building: BJT	Exercise 5.1, 5.2, 5.3
	4 Semiconductor circuit building, by	GCSE Sequential 2.1, 2.2
		GCSL Sequential 2.1, 2.2
	February Half Term	Flourabout Commonwell
21	1 Digital Coursework 1	Flowchart Coursework Semiconductors 4
24.02.25	2 Semiconductor 4	Exercise 5.4
Mon INSET	3 Digital Coursework 2	GCSE Sequential 2.3
WOTT INSET	5 Digital Coursework 2	GCSE Sequential 2.5

22	1 February Test - 50% Sequential Logic, 50% prior content	Semiconductors 5	
03.03.25	2 Digital Coursework 2	Exercise 5.5	
	3 Semiconductors 5	GCSE Sequential 2.4, 2.5	
	4 Digital Coursework 3		
23	1 February Test Feedback	Power Supplies 1	
10.03.25	2 Digital Coursework 4	Exercise 7.1	
	3 Power Supplies 1	GCSE Sequential 2.6	
	4 Digital Coursework 5	Block Diagrams	
24	1 Digital Coursework 6	Power Supplies 2	
17.03.25	2 Digital Coursework 7	Exercise 7.2	
	3 Power Supplies 2	GCSE Semiconductors 1 4.1, 4.2	
	4 Digital Coursework 8		
25	1 March Test - 50% Semiconductors, 50% prior content	Power Supplies 3	
24.03.25	2 Digital Coursework 9	Exercise 7.3	
	3 Power Supplies 3	GCSE Semiconductors 1 4.3	
	4 Digital Coursework 10	GCSE Semiconductors 2 3.2	
26	1 March Test Feedback	GCSE Power Supplies 5.1	
31.03.25	2 Digital Coursework 11		
	3 Digital Coursework 12		
	4 Digital Coursework 13		
Easter			
Digital Coursework			
Old Spec 2016, 2017, 2018, 2019 ET1 and ET2			

Summer Term				
21.04.25	2 Revision 2			
No Mon	3 Revision 3			
28	1 April Test – 50% Power Supplies, 50% prior content	AS Sample Paper		
28.04.25	2 Revision 4	·		
	3 Revision 5			
29	1 April Test Feedback	AS 2018 Paper		
05.05.25	2 Revision 6			
Mon Bank	3 Revision 7			
<mark>Holiday</mark>				
30	1 Revision 8	AS 2019 Paper		
6.5.24	Electronics Transfer Exam			
Mon Study				
Leave				
16:15				
31	Transfer Exams			
13.5.24				
	May Half Term			
32	WEX Week			
02.06.25				
33	1 Passive Filters 1: Ideal Passive Filters	Microcontrollers 1, 2		
09.6.24	2 Microcontrollers 1	Passive Filters 1, 2		
	3 Passive Filters 2: Low and High Passive Filters	Exercises 4.1		
2.4	4 Microcontrollers 2	Exercise 3.1		
34	1 Passive Filters 3: Band-Pass Filters	Microcontrollers 3, 4		
16.6.25 Tues 2A	2 Microcontrollers 3	Passive Filters 3		
Enrolment	3 Circuit Building 4 Microcontrollers 4	Exercises 4.2, 4.3		
35	1 Microcontrollers 5	Microcontrollers 5, 6		
23.06.25	2 Further Sequential Logic 1	Further Sequential Logic 1		
Fri Festival	3 Microcontrollers 6	Exercises 4.4, 4.5		
of Science	3 What dedited offers o	Exercise 3.2		
36	1 Further Sequential Logic 2	PIC Coursework 1: Planning		
30.06.25	2 PIC Coursework 1: Planning	Further Sequential Logic 2		
Mon WD	3 (Only F-Block) Extra Circuit Building Time	Exercise 1.1		
Tues WD	Town, Stocky Extra on oare Saliding Time	Excisise 111		
	1 Further Sequential Logic 3	PIC C/W 2: Development		
37	Truttier sequential Logic s			
37 07.07.25	2 PIC Coursework 2: Development	Further Sequential Logic 3, 4		
	· · · · · · · · · · · · · · · · · · ·	Further Sequential Logic 3, 4 Exercises 1.2, 1.3		
07.07.25	2 PIC Coursework 2: Development	,		